

IN THE CLAIMS

Please amend the claims as follows:

1 - 2. (Canceled)

3. (Currently Amended) The apparatus of claim [[2]] 5, wherein cooling channels are formed in a further layer over the second face of the electronics chip in fluid communication with the electro-osmotic pumps.

4. (Currently Amended) The apparatus of claim [[3]] 5, wherein external fluid connections to the pumps are made at lateral edges of the apparatus.

5. (Currently Amended) An apparatus comprising:
an electronics chip having a substrate with a first face thereof having circuitry thereon,
and an opposite second face;

one or more electro-osmotic pumps in a layer over the second face and wherein the
electro-osmotic pumps include capillary pump channels in a further layer over the second face of
the electronics chip and The apparatus of claim 2, wherein electrical power for the electro-
osmotic pumps is conducted by electrical conductors formed through the electronics chip.

6. (Currently Amended) The apparatus of claim [[2]] 5, wherein cooling channels are formed in a further layer of material over the second face of the electronics chip, and the electro-osmotic pumps are in fluid communication with the cooling channels.

7. (Original) The apparatus of claim 6, wherein external fluid connections to the pumps are made at lateral edges of the apparatus.

8. (Currently amended) An apparatus comprising:

an electronics chip having a substrate with a first face thereof having circuitry thereon, and an opposite second face; and

one or more electro-osmotic pumps in a layer over the second face.

wherein the electro-osmotic pumps include capillary pump channels in a further layer over the second face of the electronics chip and wherein electrical power for the electro-osmotic pumps is conducted by electrical conductors through the electronics chip to the pumps.

9. (Currently Amended) An apparatus comprising:

an electronics chip having a substrate with a first face thereof having circuitry thereon, and an opposite second face;

one or more electro-osmotic pumps in a layer over the second face; and

cooling channels formed in the same layer as capillary pump channels and wherein electrical power for the electro-osmotic pumps is conducted by electrical conductors formed through the electronics chip.

10. (Canceled)

11. (Currently Amended) The apparatus of claim [[10]] 12, wherein external fluid connections are made at lateral edges of the apparatus.

12. (Currently Amended) An apparatus comprising:

an electronics chip having a substrate with a first face thereof having circuitry thereon, and an opposite second face; and

one or more electro-osmotic pumps in a layer over the second face and

wherein the electronics chip is silicon, cooling channels are formed in a layer of silicon over the second face of the electronics chip, and the electro-osmotic pumps are formed in a further layer of silicon over the second face of the silicon chip in fluid communication with the cooling channels

The apparatus of claim 10, wherein electrical power for the electro-osmotic pumps is conducted by electrical conductors formed through the electronics chip.

13. (Currently Amended) An apparatus comprising:

an electronics chip made of silicon having a substrate with a first face thereof having circuitry thereon, and an opposite second face; and

one or more electro-osmotic pumps in a layer over the second face, and the electro-osmotic pumps include capillary pump channels formed in a layer of silicon over the second face of the chip and The apparatus of claim, 2-wherein electrical power for the electro-osmotic pumps is conducted by electrical conductors formed through the electronics chip.

14. (Previously Presented) An apparatus comprising:

an electronics chip having a substrate with a first face thereof having circuitry thereon, and an opposite second face and wherein the chip includes circuitry for at least a portion of a processor;

one or more electro-osmotic pumps in a layer over the second face,
a memory operatively coupled to the processor;
an input/output system, including a display unit, operatively coupled to the processor; and
a power supply operatively coupled to the processor.

15. (Previously Presented) An apparatus comprising:

an electronics chip having a substrate with a first face thereof having circuitry thereon, and an opposite second face and wherein the chip includes circuitry for at least a portion of a telecommunications circuit

one or more electro-osmotic pumps in a layer over the second face;
an antenna operatively coupled to the telecommunications circuit;
an input/output system, including a display unit, operatively coupled to the telecommunications circuit; and
a power supply operatively coupled to the telecommunications circuit.

16. - 26. (Canceled)

27. (Currently Amended) An apparatus comprising:

an electronics chip; and

an electro-osmotic pump for circulating cooling fluid through cooling channels adjacent a face of the chip wherein the electro-osmotic pump and the cooling channel are in separate layers of material attached to the face of the chip and wherein electrical power for the electro-osmotic pumps is conducted by electrical conductors formed through the electronics chip.

28. (Currently Amended) An apparatus comprising:

an electronics chip; and

an electro-osmotic pump for circulating cooling fluid through cooling channels adjacent a face of the chip wherein the electro-osmotic pump and the cooling channel channels are in the same layer of material and wherein electrical power for the electro-osmotic pumps is conducted by electrical conductors formed through the electronics chip.

29. (Currently Amended) The apparatus of claim 28, wherein the electro-osmotic pumping means pump and the cooling channel are in substantially the same plane.